# EXE: Automatically Generating Inputs of Death

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## ABSTRACT

This paper presents EXE, an effective bug-finding tool that automatically generates inputs that crash real code. Instead of running code on manually or randomly constructed input, EXE runs it on symbolic input initially allowed to be "anything." As checked code runs, EXE tracks the constraints on each symbolic (i.e., input-derived) memory location. If a statement uses a symbolic value, EXE does not run it, but instead adds it as an input-constraint; all other statements run as usual. If code conditionally checks a symbolic expression, EXE forks execution, constraining the expression to be true on the true branch and false on the other. Because EXE reasons about all possible values on a path, it has much more power than a traditional runtime tool: (1) it can force execution down any feasible program path and (2) at dangerous operations (e.g., a pointer dereference), the current path constraints allow *any* value that causes a bug. When a path terminates or hits a bug, EXE automatically generates a test case by solving the current path constraints to find concrete values using its own co-designed constraint solver, STP. Because EXE's constraints have no approximations, feeding this concrete input to an uninstrumented version of the checked code will cause it to follow the same path and hit the same bug (assuming deterministic code).

EXE works well on real code, finding bugs along with inputs that trigger them in: three Linux file systems, the `udhcpd` DHCP server, the `pcre` regular expression library, and the BSD and Linux packet filter implementations.

## Categories and Subject Descriptors

D.2.5 [**Software Engineering**]: Testing and Debugging—*Testing tools, Symbolic execution*

## General Terms

Reliability, Languages

## Keywords

Bug finding, test case generation, constraint solving, symbolic execution, dynamic analysis, attack generation.


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## 1. INTRODUCTION

Attacker-exposed code is often a tangled mess of deeply-nested conditionals, labyrinthine call chains, huge amounts of code, and frequent, abusive use of casting and pointer operations. For safety, this code must exhaustively vet input received directly from potential attackers (such as system call parameters, network packets, even data from USB sticks). However, attempting to guard against all possible attacks adds significant code complexity and requires awareness of subtle issues such as arithmetic and buffer overflow conditions, which the historical record unequivocally shows programmers reason about poorly.

Currently, programmers check for such errors using a combination of code review, manual and random testing, dynamic tools, and static analysis. While helpful, these techniques have significant weaknesses. The code features described above make manual inspection even more challenging than usual. The number of possibilities makes manual testing far from exhaustive, and even less so when compounded by programmer's limited ability to reason about all these possibilities. While random "fuzz" testing [34] often finds interesting corner case errors, even a single equality conditional can derail it: satisfying a 32-bit equality in a branch condition requires correctly guessing one value out of four billion possibilities. Correctly getting a sequence of such conditions is hopeless. Dynamic tools require test cases to drive them, and thus have the same coverage problems as both random and manual testing. Finally, while static analysis benefits from full path coverage, the fact that it inspects rather than executes code means that it reasons poorly about bugs that depend on accurate value information (the exact value of an index or size of an object), pointers, and heap layout, among many others.

This paper describes EXE ("EXecution generated Executions"), an unusual but effective bug-finding tool built to deeply check real code. The main insight behind EXE is that code can *automatically* generate its own (potentially highly complex) test cases. Instead of running code on manually or randomly constructed input, EXE runs it on *symbolic* input that is initially allowed to be "anything." As checked code runs, if it tries to operate on symbolic (i.e., input-derived) expressions, EXE replaces the operation with its corresponding input-constraint; it runs all other operations as usual. When code conditionally checks a symbolic expression, EXE forks execution, constraining the expression to be true on the true branch and false on the other. When a path terminates or hits a bug, EXE automatically generates a test case that will run this path by solving the path's con-

straints for concrete values using its co-designed constraint solver, STP.

EXE amplifies the effect of running a single code path since the use of STP lets it reason about *all possible values* that the path could be run with, rather than a single set of concrete values from an individual test case. For instance, a dynamic memory checker such as Purify [29] only catches an out-of-bounds array access if the index (or pointer) has a specific concrete value that is out-of-bounds. In contrast, EXE identifies this bug if there is any possible input value on the given path that can cause an out-of-bounds access to the array. In addition, for an arithmetic expression that uses symbolic data, EXE can solve the associated constraints for values that cause an overflow or a division/modulo by zero. Moreover, for an `assert` statement, EXE can reason about all possible input values on the given path that may cause the assert to fail. If the `assert` does not fail, then either (1) no input on this path can cause it to fail, (2) EXE does not have the full set of constraints, or (3) there is a bug in EXE.

The ability to automatically generate input to execute paths has several nice features. First, EXE can test any code path it wishes (and given enough time, exhaust all of them), thereby getting coverage out of practical reach fom random or manual testing. Second, EXE generates actual attacks. This ability lets it show that external forces can exploit a bug, improving on static analysis, which often cannot distinguish minor errors from showstoppers. Third, the EXE user sees no false positives: re-running input on an uninstrumented copy of the checked code either verifies that it hits a bug or automatically discards it if not.

Careful co-design of EXE and STP has resulted in a system with several novel features. First, STP primitives let EXE build constraints for all C expressions with perfect accuracy, down to a single bit. (The one exception is floating-point, which STP does not handle.) EXE handles pointers, unions, bit-fields, casts, and aggressive bit-operations such as shifting, masking, and byte swapping. Because EXE is dynamic (it runs the checked code) it has access to all the information that a dynamic analysis has, and a static analysis typically does not. All non-symbolic (i.e., *concrete*) operations happen exactly as they would in uninstrumented code, and produce exactly the same values: when these values appear in constraints they are correct, not approximations. In our context, what this accuracy means is that if (1) EXE has the full set of constraints for a given path, (2) STP can produce a concrete solution from those constraints, and (3) the path is deterministic, then rerunning the checked system on these concrete values will force the program to follow the same exact path to the error or termination that generated this set of constraints.

In addition, STP provides the speed needed to make perfect accuracy useful. Aggressive customization makes STP often 100 times faster than more traditional constraint solvers while handling a broader class of examples. Crucially, STP efficiently reasons about constraints that refer to memory using symbolic pointer expressions, which presents more challenges than one may expect. For example, given a concrete pointer `a` and a symbolic variable `i` with the constraint $0 \leq i \leq n$, then the conditional expression `if(a[i] == 10)` is essentially equivalent to a big disjunction: `if(a[0] == 10 || ... || a[n] == 10)`. Similarly, an assignment `a[i] = 42` represents a potential assignment to any element in the array between `0` and `n`.

The result of these features is that EXE finds bugs in real code, and automatically generates concrete inputs to trigger them. It generates evil packet filters that exploit buffer overruns in the very mature and audited Berkeley Packet Filter (BPF) code as well as its Linux equivalent (§ 5.1). It generates packets that cause invalid memory reads and writes in the `udhcpd` DHCP server (§ 5.2), and bad regular expressions that compromise the `pcre` library (§ 5.3), previously audited for security holes. In prior work it generated raw disk images that, when mounted by a Linux kernel, would crash it or cause a buffer overflow [44].

Both EXE and STP are contributions of this paper, which is organized as follows. We first give an overview of the entire system (§ 2), then describe STP and its key optimizations (§ 3), and do the same for EXE (§ 4). Finally, we present results (§ 5), discuss related work (§ 6), and conclude (§ 7).

## 2. EXE OVERVIEW

This section gives an overview of EXE. We illustrate EXE's main features by walking the reader through the simple code example in Figure 1. When EXE checks this code, it explores each of its three possible paths, and finds two errors: an illegal memory write (line 12) and a division by zero (line 16). Figure 2 gives a partial transcript of a checking run.

To check their code with EXE, programmers only need to mark which memory locations should be treated as holding *symbolic data* whose values are initially entirely unconstrained. These memory locations are typically the input to the program. In the example, the call `make_symbolic(&i)` (line 4) marks the four bytes associated with the 32-bit variable `i` as symbolic. They then compile their code using the EXE compiler, `exe-cc`, which instruments it using the CIL source-to-source translator [35]. This instrumented code is then compiled with a normal compiler (e.g., `gcc`), linked with the EXE runtime system to produce an executable (in Figure 2, `./a.out`), and run.

As the program runs, EXE executes each feasible path, tracking all constraints. When a program path terminates, EXE calls STP to solve the path's constraints for concrete values. A path terminates when (1) it calls `exit()`, (2) it crashes, (3) an assertion fails, or (4) EXE detects an error. Constraint solutions are literally the concrete bit values for an input that will cause the given path to execute. When generated in response to an error, they provide a concrete attack that can be launched against the tested system.

The EXE compiler has three main jobs. First, it inserts checks around every assignment, expression, and branch in the tested program to determine if its operands are concrete or symbolic. An operand is defined to be concrete if and only if all its constituent bits are concrete. If all operands are concrete, the operation is executed just as in the uninstrumented program. If any operand is symbolic, the operation is not performed, but instead passed to the EXE runtime system, which adds it as a constraint for the current path. For the example's expression `p = (char *)a + i * 4` (line 8), EXE checks if the operands `a` and `i` on the right hand side of the assignment are concrete. If so, it executes the expression, assigning the result to `p`. However, since `i` is symbolic, EXE instead adds the constraint that $p$ equals $(char*)a + i * 4$. Note that because $i$ can be one of four values ($0 \leq i \leq 3$), $p$ simultaneously refers to four different locations $a[0]$, $a[1]$, $a[2]$ and $a[3]$. In addition, EXE treats memory as untyped bytes (§ 3.2) and thus does

```
1 : #include <assert.h>
2 : int main(void) {
3 :   unsigned i, t, a[4] = { 1, 3, 5, 2 };
4 :   make_symbolic(&i);
5 :   if(i >= 4)
6 :     exit(0);
7 :   // cast + symbolic offset + symbolic mutation
8 :   char *p = (char *)a + i * 4;
9 :   *p = *p - 1; // Just modifies one byte!
10:
11:   // ERROR: EXE catches potential overflow i=2
12:   t = a[*p];
13:   // At this point i != 2.
14:
15:   // ERROR: EXE catches div by 0 when i = 0.
16:   t = t / a[i];
17:   // At this point: i != 0 && i != 2.
18:
19:   // EXE determines that neither assert fires.
20:   if(t == 2)
21:     assert(i == 1);
22:   else
23:     assert(i == 3);
24: }
```

Figure 1: A contrived, but complete C program that generates five test cases when run under EXE, two of which trigger errors (a memory overflow at line 12 and a division by zero at line 16). This example is used heavily throughout the paper. We assume it runs on a 32-bit little-endian machine.

```
% exe-cc simple.c
% ./a.out
% ls exe-last
  test1.forks test2.out     test3.forks  test4.out
  test1.out   test2.ptr.err test3.out    test5.forks
  test2.forks test3.div.err test4.forks  test5.out
% cat exe-last/test3.div.err
  ERROR: simple.c:16 Division/modulo by zero!
% cat exe-last/test3.out
  # concrete byte values:
  0 # i[0]
  0 # i[1]
  0 # i[2]
  0 # i[3]
% cat exe-last/test3.forks
  # take these choices to follow path
  0 # false branch (line 5)
  0 # false (implicit: pointer overflow check on line 9)
  1 # true (implicit: div-by-0 check on line 16)
% cat exe-last/test2.out
  # concrete byte values:
  2 # i[0]
  0 # i[1]
  0 # i[2]
  0 # i[3]
```

Figure 2: Transcript of compiling and running the C program shown in Figure 1.

not get confused by this (dubious) cast, nor the subsequent type-violating modification of a low-order byte at line 9.

Second, `exe-cc` inserts code to fork program execution when it reaches a symbolic branch point, so that it can explore each possibility. Consider the if-statement at line 5, `if(i >= 4)`. Since `i` is symbolic, so is this expression. Thus, EXE forks execution (using the UNIX `fork()` system call) and on the true path asserts that $i \geq 4$ is true, and on the false path that it is not. Each time it adds a branch constraint, EXE queries STP to check that there exists at least one solution for the current path's constraints. If not, the path is impossible and EXE stops executing it. In our example, both branches are possible, so EXE explores both (though the true path exits immediately at line 6).

Third, `exe-cc` inserts code that calls to check if a symbolic expression could have any possible value that could cause either (1) a null or out-of-bounds memory reference or (2) a division or modulo by zero. If so, EXE forks execution and (1) on the true path asserts that the condition does occur, emits a test case, and terminates; (2) on the false path asserts that the condition does not occur and continues execution (to find more bugs). Extending EXE to support other checks is easy. If EXE has the entire set of constraints on such expressions and STP can solve them, then EXE will detect if *any* input exists on that path that can cause the error. Similarly, if the check passes, then no input exists that causes the error on that path — i.e., the path has been *verified* as safe under all possible input values.

These checks find two errors in our example. First, the symbolic index `*p` in the expression `a[*p]` (line 12) can cause an out-of-bounds error because `*p` can equal 4: the pointer `p` was computed using `i` with the constraint $0 \leq i < 4$ (line 8). Thus, $i = 2$ is legal, which means `p` can point to the low-order byte of `a[2]` (recall that each element of `a` has four bytes). The value of this byte is 4 after the subtraction at line 9. Since `a[4]` references an illegal location one past the end of `a`, EXE forks execution and on one path asserts that $i = 2$ and emits an error (`test2.ptr.err`) and a test case (`test2.out`), and on the other that $i \neq 2$ and continues.

Second, the symbolic expression `t / a[i]` (line 16) can divide by zero, which EXE detects by tracking and solving the constraints that (1) `i` can equal 0, 1, or 3 and (2) $a[0]$ can equal 0 after the decrement at line 9. EXE again forks execution, emits an error (`test3.div.err`) and a test case (`test3.out`) and exits. The other path adds the constraint that $i \neq 0$ and continues.

Note, EXE automatically turns a programmer `assert(e)` on a symbolic expression $e$ into a universal check of $e$ simply because it tries to exhaust both paths of if-statements. If EXE determines $e$ can be false, it will go down the assertion's false path, hitting its error handling code. Further, if STP cannot find any such value, none exists on this path. In the example, EXE explores both branches at line 20, and proves that no input value exists that can cause either `assert` (line 21 and line 23) to fail. We leave working through this logic as an exercise for the more energetic reader. Even a cursory attempt should show the trickiness of manual reasoning about all-paths and all-values for even trivial code fragments. (We spent more time than we would like to admit puzzling over our own hand-crafted example and eventually gave up, resorting to using EXE to double-check our oft-wrong reasoning.)

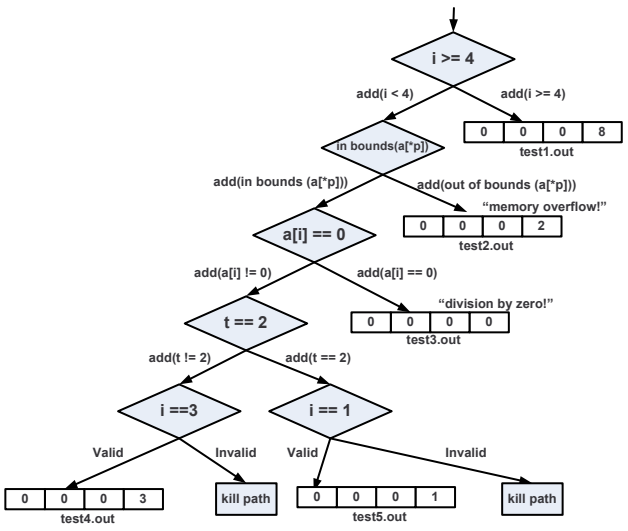


Figure 3: Execution for simple.c: generates five test cases, two of which are errors.

The paths followed by EXE are shown graphically in Figure 3. The branch points (both explicit and implicit) where EXE forks a new process are represented by rhombuses, and the test cases it generates by sequences of four bytes.

Mechanically, at each run of the instrumented code, EXE creates a new directory and, for each path, creates two files: one to hold the concrete bytes it generates, the other to hold the values for each decision (0 to take the true branch, 1 to take the false). The choice points enable easy replay of a single path for debugging. The values can either be read back by using a trivial driver (which EXE provides) or used completely separately from EXE.

In our example, the three paths and two errors lead to five pairs of files that hold (1) concrete byte values for `i` (these files have the suffix "`.out`") and (2) the branch decisions for that path (suffix "`.forks`"). EXE creates a symbolic link `exe-last` pointing to the most recent output directory. The two errors are in `.err` files. If we look at the contents of the file for the division bug (`test3.out`), it shows that each byte of `i` is zero, which when concatenated in the right order and treated as an unsigned 32-bit quantity equals `0`, as required. The branch decision states that we take the false branch at line 5, followed by the (implicit) false branch of the memory overflow check at line 9, and finally the (implicit) true branch of the division check at line 16. Similarly, the concrete values for the pointer error are byte 0 equals 2 and bytes 1, 2, 3 equal 0, which when concatenated yields the 32-bit value 2 as needed.

# 3. KEY FEATURES OF STP

This section gives a high-level overview of STP's key features, including the support it provides to EXE for accurately modeling memory. It then describes the optimizations STP performs, and shows experimental numbers evaluating their efficiency.

EXE's constraint solver is, more precisely, a *decision procedure* for bitvectors and arrays. Decision procedures are programs which determine the satisfiability of logical formulas that can express constraints relevant to software and hardware, and have been a mainstay of program verification for several decades. In the past, these decision procedures have been based on variations of Nelson and Oppen's *cooperating decision procedures framework* [36] for combining a collection of specialized decision procedures into a more comprehensive decision procedure for a more expressive logic than any of the specialized procedures can handle individually.

The Nelson-Oppen approach has two downsides. Whenever a specialized decision procedure can infer that two expressions are equal, it must do so explicitly and communicate the equality to the other specialized decision procedures, which can be expensive. Worse, the framework tends to lead to a web of complex dependencies, which makes its code difficult to understand, tune, or get right. These problems hampered CVCL [6], a state-of-the-art decision procedure that we implemented previously.

Our CVCL travails motivated us to simplify the design of STP by exploiting the extreme improvement in SAT solvers over the last decade. STP forgoes Nelson-Oppen contortions, and instead preprocesses the input through the application of mathematical and logical identities, and then eagerly translates constraints into a purely propositional logic formula that it feeds to an off-the-shelf SAT solver (we use MiniSAT [20]). As a result, the STP implementation has five times less code than CVCL, yet often runs orders of magnitude faster. STP is also more modular, because its pieces work in isolation. Modularity and simplicity help constraint solvers as they do everything else. In a sense, STP can be viewed as the result of applying the systems approach to constraint solving that has worked so well in the context of SAT: start simple, measure bottlenecks on real workloads, and tune to exactly these cases. STP was recently judged the co-winner of the QF_UFBV32 division of the SMTLIB competition [1] held as a satellite event of CAV 2006 [3].

Recently, several other decision procedures have been based on eager translation to SAT, including Saturn[43], UCLID[10], and Cogent[14]. Saturn is a static program analysis framework that translates C operations to SAT. It does not directly deal with arrays, so it avoids many interesting problems and optimizations. UCLID implements features such as arrays and arbitrary precision integer arithmetic, but does not focus on bitvector operations. Cogent is perhaps the most similar in architecture and purpose to STP. Judging from the published descriptions of these systems, STP's focus on optimizations for arrays is unique (and uniquely important for use with EXE). STP also has simplifications on word-level operations that are not discussed in the description of Cogent. (At this time, it is difficult to do side-by-side performance comparisons because of lack of common benchmarks and input syntax; Saturn, UCLID and Cogent also didn't participate in the SMTLIB competition.)

## 3.1 STP primitives

System code often treats memory as untyped bytes, and observes a single memory location in multiple ways. For example, by casting signed variables to unsigned, or (in the code we checked) treating an array of bytes as a network packet, inode, packet filter, etc. through pointer casting.

As a result, STP also views memory as untyped bytes. It provides only three data types: booleans, bitvectors, and arrays of bitvectors. A bitvector is an unsigned, fixed-length sequence of bits. For example, "`0010`" is a constant, 4-bit

bitvector representing the constant 2. With the exception of floating-point, which STP does not support, all C operators have a corresponding STP operator that can be used to impose constraints on bitvectors. STP implements all arithmetic operations (even non-linear operations such as multiplication, division and modulo), bitwise boolean operations, relational operations (less than, less than or equal, etc.), and multiplexers, which provide an "if-then-else" construct that is converted into a logical formula (similar to C's ternary operator). In addition, STP supports bit concatenation and bit extraction, features EXE makes extensive use of in order to translate untyped memory into properly-typed constraints.

STP implements its bitvector operations by translating them to operations on individual bits. There are two expression types: *terms*, which have bitvector values, and *formulas*, which have boolean values. If $x$ and $y$ are 32-bit bitvector values, $x + y$ is a term returning a 32-bit result, and $x + y < z$ is a formula. In the implementation, terms are converted into vectors of boolean formulas consisting entirely of single bit operations (AND, XOR, etc.). Each operation is converted in a fairly obvious way: for example, a 32-bit add is implemented as a ripple-carry adder. Formulas are converted into DAGs of single bit operations, where expressions with identical structure are represented uniquely (expression nodes are looked up in a hash table whenever they are created to see whether an identical node already exists). Simple boolean optimizations are applied as the nodes are created; for example, a call to create a node for `AND(x, FALSE)` will just return the `FALSE` node. The resulting boolean DAG is then converted to CNF by the standard method of naming intermediate nodes with new propositional variables.

## 3.2 Mapping C code to STP constraints

EXE represents each symbolic data block as an array of 8-bit bitvectors. The main advantage of using bitvectors is that they, like the C memory blocks that they represent, are essentially untyped. This property allows us to easily express constraints that refer to the same memory in different ways; each read of memory generates constraints based on the static type of the read (e.g., `int`, `unsigned`, etc.) but these types do not persist.

EXE uses STP to solve constraints on input as follows. First, it tracks what memory locations in the checked code hold symbolic values. Second, it translates expressions to bitvector based constraints. We discuss each step below.

Initially, there are no symbolic bytes in the checked code. When the user marks a byte-range, `b`, as symbolic, EXE calls into STP to create a corresponding, identically-sized array $b_{sym}$, and records in a table that `b` corresponds to $b_{sym}$. In Figure 1 (line 4), the call to make the 32-bit variable `i` symbolic causes EXE to allocate a bitvector array $i_{sym}$ with four 8-bit elements and record that the concrete address of `i` (`&i`) corresponds to it.

As the program executes, the table mapping concrete bytes to STP bitvectors grows in exactly two cases:

1. `v = e`: where `e` is a symbolic expression (i.e., has at least one symbolic operand). EXE builds the symbolic expression $e_{sym}$ representing `e`, and records that `&v` maps to it. Note that EXE does not allocate a new STP variable in this case but instead will substitute $e_{sym}$ for `v` in subsequent constraints. EXE removes this mapping when `v` is overwritten with a concrete value or deallocated. In Figure 1 (line 8), EXE records the fact that `p` maps to a constraint representing the symbolic expression $(char*)a + i_{sym} * 4$ and substitutes any subsequent use of `p`'s value with this constraint. (Note that $a$ is replaced by the actual base address of array `a` in the program.)

2. `b[e]`: where `e` is a symbolic expression and `b` is a concrete array. Since STP must reason about the set of values that `b[e]` could reference, EXE imports `b` into STP by allocating an identically-sized STP array $b_{sym}$, and initializing it to have the same (constant) contents as `b`. It then records that `b` maps to $b_{sym}$ and removes this mapping only when the array is deallocated.

   In Figure 1 (line 12), the array expression `a[*p]` causes EXE to allocate $a_{sym}$, a 16-element array of 8-bit bitvectors, and assert that:

$$a_{sym} = \{1, 0, 0, 0, 3, 0, 0, 0, 5, 0, 0, 0, 2, 0, 0, 0\}$$

Each expression `e` used in a symbolic operation is constructed in the following way. For each read of size $n$ of a storage location $l$ in $e$, EXE checks if $l$ is concrete. If so, the read of $l$ is replaced by its concrete value (i.e., a constant). Otherwise, EXE breaks down $l$ into its corresponding bytes $b_0, \ldots, b_{n-1}$. It then builds a symbolic expression with the same size as $l$ by concatenating each byte's (possibly symbolic) value. For each byte $b_i$ it queries its data structures to check if $b_i$ is symbolic. If not, it uses its current concrete value (an 8-bit constant), otherwise it looks up and uses its symbolic expression $(b_i)_{sym}$.

For example, in Figure 1 (line 8), EXE builds the symbolic expression corresponding to `(char*)a + i*4` as follows. EXE determines that the first read of `a`, is concrete and so replaces `a` with its concrete address (denoted $a$) represented as a 32-bit bitvector constant. It then determines that `i` is symbolic, and thus breaks it down into its four bytes, which are mapped to their corresponding STP bitvector array elements $i_{sym}[0]$, $i_{sym}[1]$, $i_{sym}[2]$, and $i_{sym}[3]$. Then, the four bitvectors are concatenated to obtain the expression $i_{sym}[3]$ @ $i_{sym}[2]$ @ $i_{sym}[1]$ @ $i_{sym}[0]$ (where "@" denotes bitvector concatenation), which corresponds to the four-byte read of `i`. Finally, the constant `4` is replaced by the corresponding 32-bit bitvector constant 0...00000100. The resulting expression is

$$a + (i_{sym}[3] @ i_{sym}[2] @ i_{sym}[1] @ i_{sym}[0]) * 0...00000100$$

A limitation of STP is that it does not support pointers directly. EXE emulates symbolic pointer expressions by mapping them as an array reference at some offset. For each pointer `p` in the checked code, EXE tracks the data object to which `p` points by instrumenting all allocation and deallocation sites as well as all pointer arithmetic expressions (standard techniques developed by bounds-checking compilers [39]). For example, in Figure 1 (line 4), EXE records that `p` points to the data block `a` of size 16. Then, when EXE encounters a pointer dereference `*p`: (1) it looks up the block `b` to which pointer `p` refers; (2) looks up the corresponding STP array $b_{sym}$ associated with `b`; and (3) computes the (possibly symbolic) offset of `p` from the base of the object it points to (i.e., `o = p - b`). EXE can then uses the symbolic expression $b_{sym}[i_{sym} + o_{sym}]$ in symbolic constraints.

However, STP's lack of pointer support means that when EXE encounters a double-dereference `**p` of a symbolic pointer `p` it *concretizes* the first dereference (`*p`), fixing it to one of the possibly many storage locations it could refer to. (However, the result of `**p` can still be a symbolic expression.) This situation has rarely shown up in practice (see § 4.3, but we are working on removing it.

## 3.3 The key to speed: fast array constraints

The main bottleneck in STP when used in EXE is almost always reasoning about arrays. This subsection discusses STP's key array optimizations.

STP is an implementation of logic, so it is a purely functional language. The logic has one-dimensional arrays that are indexed by bitvectors and contain bitvectors. The operations on arrays are $read(A, i)$, which returns the value at location $A[i]$ where $A$ is an array and $i$ is an index expression of the correct type, and $write(A, i, v)$, which returns a new array with the same value as $A$ at all indexes except $i$, where it has the value $v$. Array reads and writes can appear as subexpressions of an `if-then-else` construct, denoted by $ite(c, a, b)$, where $c$ is the condition, $a$ the `then` expression, and $b$ the `else` expression.

STP eliminates array expressions by translating them to bitvector primitives (which it then translates to SAT). This is accomplished through two main transformations. The first, **read-over-write**, eliminates all $write(A, i, v)$ expressions: [1]

$$read(write(A, i, v), j) \Rightarrow ite(i = j, v, read(A, j))$$

The second eliminates all *read* expressions via a transformation mentioned in [10] that enforces the axiom that if two indexes $i_1$ and $i_2$ are the same then $read(A, i_1)$ and $read(A, i_2)$ should return the same value. Mechanically, STP first replaces each occurrence of a read $read(A, i_j)$ with a new variable $v_j$, and then for each two terms $i_1, i_2$ ever used to index the same array $A$, it adds the *array axiom*:

$$i_1 = i_2 \Rightarrow v_1 = v_2$$

For example, consider the formula:

$$(read(A, i_1) = e_1) \wedge (read(A, i_2) = e_2) \wedge (read(A, i_3) = e_3)$$

The transformed result would be:

$$(v_1 = e_1) \wedge (v_2 = e_2) \wedge (v_3 = e_3) \wedge (i_1 = i_2 \Rightarrow v_1 = v_2) \wedge$$

$$(i_1 = i_3 \Rightarrow v_1 = v_3) \wedge (i_2 = i_3 \Rightarrow v_2 = v_3)$$

Read elimination expands each formula by $n(n-1)/2$ nodes, where $n$ is the number of syntactically distinct index expressions. Unfortunately, this blowup is lethal for arrays of a few thousand elements, which occur frequently in EXE. Fortunately, while finessing this problem appears hard in general, two optimizations we developed work well on the constraints generated by EXE.

The *array substitution optimization* reduces the number of array variables by substituting out all constraints of the form $read(A, c) = e$, where $c$ is a constant and $e$ does not contain another array read. Programs often index into arrays using constant indexes, so this is a case that occurs often in practice (see § 4.3). The optimization has two passes. The first pass builds a substitution table with the left-hand-side of each such equation ($read(A, c)$) as the key and the right-hand-side ($e$) as the value, and then deletes the equation from the EXE query. The second pass over the expression replaces each occurrence of a key by the corresponding table entry. Note that for soundness, if we encounter a second equation whose left-hand-side is already in the table, the second equation is not deleted and the table is not changed. For our example, if we saw a subsequent equation $read(A, i_1) = e_4$ we would leave it; the second pass of the algorithm would rewrite it as $e_1 = e_4$.

The second optimization, *array-based refinement*, delays the translation of array *read*s with non-constant indexes, in effect introducing some laziness into STP's handling of arrays, in the hope of avoiding the $O(n^2)$ blowup from the read elimination transformation. Its main trick is to solve a less-expensive approximation of the formula, check the result in the original formula, and try again with a more accurate approximation if the result is incorrect.

Initially, all array read expressions are replaced by variables to yield an approximation of the original formula. The resulting logical formula is under-constrained, since it ignores the array axioms that require that array reads return the same values when indexes are the same. If the resulting under-constrained formula is not satisfiable, there is no solution for the original formula and STP returns unsatisfiable.

If, however, the SAT solver finds a solution to the under-constrained formula, then that solution is not guaranteed to be correct because it could violate one of the array axioms. For example, suppose STP is given the formula $(read(A, 0) = 0) \wedge (read(A, i) = 1)$. STP would first apply the substitution optimization by deleting the constraint $read(A, 0) = 0$ from the formula, and inserting the pair $(read(A, 0), 0))$ in the substitution table. Then, it would replace $read(A, i)$ by a new variable $v_i$, thus generating the under-constrained formula $v_i = 1$. Suppose STP finds the solution $i = 1$ and $v_i = 1$. STP then translates the solution to the variables of the original formula to get $(read(A, 0) = 0) \wedge (read(A, 1) = 1))$. This solution is satisfiable in the original formula as well, so STP terminates since it has found a true satisfying assignment.

However, suppose that STP finds the solution $i = 0$ and $v_i = 1$. Under this solution, the original formula evaluates to $(read(A, 0) = 0) \wedge (read(A, 0) = 1)$, which gives $0 = 1$. Hence, the solution to the under-constrained formula is not a solution to the original formula. When this happens, it must be because some array axiom was violated. STP adds array axioms to the formula and solves again until it gets a correct result. There are many policies for adding axioms, any of which is correct and will terminate so long as all of the axioms are added in the worst case. The current policy, which seems to work well, is to find an array index term for which at least one axiom is violated, then add all of the axioms involving that term. In our example, it will add the axiom $i = 0 \Rightarrow read(A, i) = read(A, 0)$. Then, the process of finding a satisfying assignment is repeated, by calling the SAT solver on the new under-constrained formula. The result must satisfy the newly added axioms, which the previous assignment violated, so the algorithm will not repeat assignments and will not violate previously added axioms. This process must terminate since there are only finitely many substitution axioms.

In the worst case, the algorithm will add all $n(n-1)/2$

[1] Note that a *write* makes sense only inside a *read* node. A *write* node by itself has no effect, and can be ignored.

| Solver | Total Time | Timeouts |
|---|---|---|
| CVCL | 60,366s | 546 |
| STP (no optimizations) | 3,378s | 36 |
| STP (substitution) | 1,216s | 1 |
| STP (refinement) | 624s | 1 |
| STP (simplifications) | 336s | 0 |
| STP (subst+refinement) | 513s | 1 |
| STP (simplif+subst) | 233s | 0 |
| STP (simplif+refinement) | 220s | 0 |
| STP (all optimizations) | 110s | 0 |

Table 1: STP vs.CVCL. Queries timeout (are aborted) after 60 seconds, which underestimates performance differences, since they could run for much longer. Using this conservative estimate, fully optimized STP is roughly 550X faster than CVCL and has no timeouts.

array axioms, at which time it is guaranteed to return a correct result because there are no more axioms it can violate. However, in practice, this loop will often terminate quickly because the formula can be proved unsatisfiable without all the array axioms, or because it luckily finds a true satisfying assignment without adding all the axioms.

Besides the above mentioned optimizations, STP implements several Boolean and mathematical identities. These identities, or *simplifications*, also dramatically reduce the size of the input, before it is fed to the SAT solver. Some example identities include associativity and commutativity laws for addition and multiplication, distribution of multiplication by constants over addition, and combining like terms, e.g. $x + (-x)$ is simplified to 0.

All the above mentioned optimizations have made it possible to deal with fairly large constant arrays when there are relatively few non-constant index expressions, which is sufficient to permit considerable progress in using EXE on real examples.

## 3.4 Measured performance

Table 1 gives experimental measurements for these optimizations. The experiment consists of running different versions of STP and our old solver, CVCL, over the performance regression suite we have built up of 8495 test cases taken from our test programs. The experiments for all solvers were run on Pentium 4 machine at 3.2 GHz, with 2 GB RAM and 512 KB cache. The table gives the times taken by CVCL, baseline STP without optimizations, STP with a subset of all optimizations enabled, and STP with full optimizations, i.e. both simplifications, substitution and array-based refinement. The third column shows the number of examples on which each solver timed out. The timeout was set at 60 seconds, and is added as penalty to the time taken by the solver (but in fact causes us to grossly underestimate the time taken by CVCL and earlier versions of STP since they could run for many minutes or even hours on some of the examples).

The baseline STP is nearly 20 times faster than CVCL, and more interestingly times out in far fewer cases. The optimized version of STP with substitution and array-based refinement (full optimization) is almost 550 times faster, and there are no timeouts.

# 4. EXE OPTIMIZATIONS

This section presents optimizations EXE uses and measures their effectiveness on five benchmarks. We first present two optimizations: caching constraints to avoid calling STP (§ 4.1), and removing irrelevant constraints from the queries EXE sends to STP (§ 4.2). We then measure the cumulative improvement of these optimizations, and provide an empirical feel for what symbolic execution looks like, including the time spent in various parts of EXE, and a description of the symbolic slice through the code (§ 4.3). Finally, we discuss and measure EXE's search heuristics (§ 4.4).

## 4.1 Constraint caching

EXE caches the result of satisfiability queries and constraint solutions in order to avoid calling STP when possible. This cache is managed by a server process so that multiple EXE processes (created by forking at each conditional) can coordinate. Before invoking STP on a query `q`, an EXE process prints `q` as a string, computes an MD4 cryptographic hash of this string, and sends this hash to the server. The server checks its persistent cache (a file) and if it gets a hit, returns the result. If not, the EXE process does a local STP query and then sends the $(hash, val)$ pair back to the server. The server caches constraint solutions in a similar way.

## 4.2 Constraint independence optimization

This section describes one of EXE's most important optimizations, *constraint independence*, which exploits the fact that we can often divide the set of constraints EXE tracks into multiple independent subsets of constraints. Two constraints are considered to be independent if they have disjoint sets of operands (i.e. disjoint sets of array reads).

For example, assume EXE tracks the following set of three constraints:

$(A[1] = A[2] + A[3]) \wedge (A[2] > A[4]) \wedge (A[7] = A[8])$

We can divide this set into two subsets of independent constraints

$$(A[1] = A[2] + A[3]) \wedge (A[2] > A[4])$$

and

$$(A[7] = A[8]$$

and solve them separately.

Breaking a constraint into multiple independent subsets has two benefits. First, EXE can discard irrelevant constraints when it asks STP if a constraint $c$ is satisfiable, with a corresponding decrease in cost. Instead of sending all the constraints collected so far to STP, EXE only sends the set of constraints $s_c$ to which $c$ belongs, ignoring all other constraints. The worst case, when no irrelevant constraints are found, costs no more than the original query (omitting the small cost of computing the independent subsets).

Second, this optimization yields additional cache hits, since a given a set of independent constraints may have appeared individually in previous runs. Conversely, including all constraints vastly increases the chance that at least one is different and so gets no cache hit. To illustrate, assume we have the code fragment, which operates on two unconstrained symbolic arrays $A$ and $B$:

```
if (A[i] > A[i+1]) {
   ...
}
if (B[j] + B[j-1] == B[j+1]) {
   ...
}
```

There are four paths through this code; EXE will thus create four processes. After forking and following each branch,

EXE checks if the path is satisfiable. Without the constraint independence optimization, each of these four satisfiability queries will differ and miss in the cache. However, if the optimization is applied, some queries repeat. For example, when the second branch is reached, two of the four queries will be

$$(A[i] > A[i+1]) \wedge (B[j] + B[j-1] = B[j+1])$$

and

$$(A[i] \leq A[i+1]) \wedge (B[j] + B[j-1] = B[j+1])$$

which both devolve to

$$B[j] + B[j-1] = B[j+1]$$

since the first constraint is unrelated to the last one, and its satisfiability was already determined when EXE reached the first branch.

Real programs often have many independent branches, which introduce many irrelevant constraints. These add up quickly. For example assuming $n$ consecutive independent branches (the example above is such an instance for $n = 2$), EXE will issue $2(2^n - 1)$ queries to STP (for each `if` statement, we issue two queries to check if both branches are possible). The optimization exponentially reduces this query count to $2n$ (two queries the first time we see each branch), since the rest of the time we find the result in the cache.

We compute the constraint independent subsets by constructing a graph $G$, whose nodes are the set of all array reads used in the given set of constraints. For the first example in the section, the set of nodes is {$A[1]$, $A[2]$, $A[3]$, $A[4]$, $A[7]$, $A[8]$}. We add an edge between nodes $n_i$ and $n_j$ of $G$ if and only if there exists a constraint $c$ that contains both as operands. Once the graph $G$ is constructed, we apply a standard algorithm to determine $G$'s connected components. Finally, for each connected component, we create a corresponding independent subset of constraints by adding all the constraints that contain at least one of the nodes in that connected component. At the implementation level, we don't construct the graph $G$ explicitly. Instead, we keep the nodes of $G$ into a union-find structure [16], which we update each time we add a new constraint.

There are two additional issues that our algorithm has to take into account. First, an array read may contain a symbolic index. In this case, we are conservative, and merge all the elements of that array into a single subset.

The second issue relates to array writes. Since EXE and STP arrays are functional, each array read explicitly contains an ordered list of all array writes performed so far. Each array write is remembered as a pair consisting of the location that was updated, and the expression that was written to that location. When processing the array writes performed so far we are again conservative, and merge all the expressions written into the array (the right hand side of each array write) into the subset of the original read. In addition, if any array write is performed at a symbolic index, we merge all the elements of the array into a single subset.

## 4.3 Experiments

We evaluate our optimization on five benchmarks. These benchmarks consist of the three applications in which we found bugs, `bpf`, `pcre`, and `udhcpd` (and which are described in more detail in Section 5), to which we added two more: `expat`, an XML parser library, and `tcpdump`, a tool for printing out the headers of packets on a network interface that match a boolean expression.

| | bpf | expat | pcre | tcpdump | udhcpd |
|---|---|---|---|---|---|
| Test cases | 7333 | 360 | 866 | 2140 | 328 |
| None | 30.6 | 28.4 | 31.3 | 28.2 | 30.4 |
| Caching | 32.6 | 30.8 | 34.4 | 27.0 | 36.4 |
| Independence | 17.8 | 25.2 | 10.0 | 24.9 | 30.5 |
| All | 10.3 | 26.3 | 7.5 | 23.6 | 32.1 |
| STP cost | 6.9 | 24.6 | 2.8 | 22.4 | 23.1 |

Table 2: Optimization measurements, times in minutes. STP cost gives time spent in STP when all all optimizations are enabled.Table 3, Table 4, and Table 5 explore the fully optimized run (All) in more detail.

We run each benchmark under four versions of EXE: no optimization, caching only, independence only, and finally with both optimizations turned on. As a baseline we run each benchmark for roughly 30 minutes using the unoptimized version of EXE, and record the number of test cases $n$ that this run generates. We then run the other versions until they generate $n$ test cases. All experiments are performed on a dual-core 3.2 GHz Intel Pentium D machine with 2 GB of RAM, and 2048 KB of cache.

Table 2 gives the number of test cases generated, as well as the runtime for each optimization combination. Full optimization ("All") significantly sped up two of five benchmarks: `bpf` by roughly a factor of three, and `pcre` by more than a factor of four. Both `tcpdump` and `expat` had marginal improvements (20% and 7% faster respectively), but `udhcpd` slows down by 5.6%. As the last row shows, with the exception of `pcre`, the time spent in STP represents by far the dominant cost of EXE checking.

Table 3 breaks down the full optimization run. As its first three rows show, caching without independence is not a win — its overhead actually increases runtime for most applications, varying between 6.5% for `bpf` and 19.7% for `pcre`. With independence, the hit rate jumps sharply for both `bpf` and `pcre` (and, to a lesser extent, `tcpdump`), due to its removal of irrelevant constraints. The other two applications show no benefit from these optimizations — `udhcpd` has no independent constraints and `expat` has no cache hits. The average number of independent subsets (row 3) shows how interdependent our constraints are, varying from over 2,800 subsets for `expat` to only 1 (i.e., no independent constraints) for `udhcpd`.

The second three rows (4–6) measure the overhead spent in various parts of EXE. Reassuringly, the cost of independence is near zero. On the other hand, cache overhead (row 5) is significant, due almost entirely to our naive implementation. On each cache lookup (§ 4.1), EXE prints the query as a string and then hashes it. As the table shows (row 6) the cost of printing the string dominates all other caching overheads. Obviously we plan to eliminate this inefficiency in the next version of the system.

Table 4 breaks down the queries sent to STP. The first three rows give the total number of: queries, constraints, and nodes. The ratio of these last numbers gives a feel for query complexity: `bpf` is the easiest case (a small number of constraints, with roughly five nodes per constraint), whereas `udhcpd` is the worst with 688 nodes per constraint.

The next two rows give the number of non-linear constraints (row 4) and their percentage (row 5) of the total

| # | | bpf | expat | pcre | tcpdump | udhcpd |
|---|---|---|---|---|---|---|
| 1 | Cache hit rate | 92.8% | 0% | 83% | 35% | 9.1% |
| 2 | Hit rate w/o independence | 0.1% | 0% | 17.5% | 12.6% | 9.1% |
| 3 | Avg. # of independent subsets | 19 | 2,824 | 122 | 13 | 1 |
| 4 | Independence overhead | 0m | 0m | .1m | 0m | 0m |
| 5 | Cache lookup cost | 1.1m | 1.2m | 1.9m | 0.4m | 2.1m |
| 6 | % of lookup spent printing | 72% | 96% | 84% | 90% | 95% |

Table 3: Optimization breakdown

| # | Statistic | bpf | expat | pcre | tcpdump | udhcpd |
|---|---|---|---|---|---|---|
| 1 | # of queries (cache misses) | 162,959 | 5,427 | 188,481 | 22,242 | 3,572 |
| 2 | Total # of constraints | 402,496 | 9,649,411 | 3,478,517 | 1,268,316 | 626, 795 |
| 3 | Total # of nodes | 2,048,704 | 32,711,503 | 17,844,792 | 20,673,550 | 431,705,056 |
| 4 | # non-linear constraints | 3,758 | 10,679 | 95,623 | 343,312 | 508,096 |
| 5 | % constraints non-linear | 0.9% | 0.1% | 2.8% | 27.1% | 81.1% |
| 6 | Reads from symbolic array | 405,501 | 11,788,264 | 3,757,238 | 1,619,843 | 3,855, 965 |
| 7 | % sym. array reads with sym. index | 0.3% | 0.3% | 2.9% | 7.8% | 62.9% |
| 8 | Writes to symbolic array | 62 | 2,310,903 | 706,214 | 0 | 0 |
| 9 | % sym. array writes with sym. index | 100% | 0% | 1.8% | 0% | 0% |

Table 4: Dynamic counts from queries sent to STP.

constraints (from row 2). Non-linear constraints contain one or more non-linear operators — multiplication, division, or modulo (all by non-powers of two). In general, the more non-linear operations, the slower constraint solving gets, as the SAT circuits that STP constructs for these operations are expensive. For our benchmarks, only `udhcpd` has a large number of non-linear constraints, which translates into a large amount of time spent in STP.

The final four rows (6–9) give the number of reads and writes from and to symbolic data blocks, and the percentage of these that use symbolic indexes. While there are many array operations, very few use symbolic indexes, which explains why the STP array substitution optimization (§ 3.3) was such a big win.

Table 5 gives dynamic execution counts from the full optimization runs. The first row gives the number of bytes initially marked as symbolic; this represents the size of the symbolic filter and data in `bpf`, the size of the XML expression to be parsed in `expat`, the packet length in `udchpd` and `tcpdump`, and the regular expression pattern length in `pcre`.

The next row (row 2) gives the total number of dynamic statements executed (assignments, branches, parameter and return value passing) across all paths executed by EXE, while the next (row 3) gives the percentage that are symbolic. For our benchmarks, this percentage varies from only 8.46% for `expat` to 41.70% for `tcpdump`. This numbers are encouraging and validate our approach of mixing concrete and symbolic, which lets us ignore a large amount of code in the checked programs.

The next three rows (4–6) look at symbolic branches, including the implicit branches EXE does for checking. Row 4 gives the total number of explicit symbolic branch points and row 5 the percentage of these branch points that had both branches feasible. (The others EXE pruned because the path's constraints were not satisfiable.) On our benchmarks, EXE was able to prune more than 80% of the branches it encountered, with the exception of `udhcpd` where it pruned (only) 47.18% of the branches. These results are reassuring for scalability – while the potential number of paths in the search space grows exponentially with the number of symbolic branches, the actual growth is much smaller: real code appears to have many dependencies between program points.

Row 6 measures the average number of symbolic branches per path. This number is large: ranging from around 38 up to 200 branches, which means that random guessing would have a hard time satisfying all the branches to get to the end of one path, much less the hundreds or thousands that EXE can systematically explore.

Row 7 gives the total number of times EXE performed a symbolic check. Row 8 shows how many times EXE had to concretize a pointer because it encountered a symbolic dereference of a symbolic pointer (§ 3.2). This situation occurs in only one of our five benchmarks, `tcpdump`. Finally, row 9 shows that no uninstrumented functions were called with symbolic data as arguments.

## 4.4 Search heuristics

When EXE forks execution, it must pick which branch to follow first. By default, EXE uses depth-first search (DFS), picking randomly between the two branches. DFS keeps the current number of processes small (linear in the depth of the process chain), but works poorly in some cases. For example, if EXE encounters a loop with a symbolic variable as a bound, DFS can get "stuck" since it attempts to execute the loop as many times as possible, thus potentially taking a very long time to exit the loop.

In order to overcome this problem, we use search heuristics to drive the execution along "interesting" execution paths (e.g., that cover unexplored statements). After a `fork` call, each forked EXE process calls into a search server with a description of its current state (i.e., its current file, line number, and backtrace) and blocks until the server replies. The search server examines all blocked processes and picks the best one in terms of some heuristic that is more global than simply picking a random branch to follow. Our current heuristic uses a mixture of best-first and depth-first search. The search server picks the process blocked at the line of code run the fewest number of times and then runs this process (and its children) in a DFS manner for a while. It then picks another best-first candidate and iterates. This is just one of many possible heuristics, and the server is structured so that new heuristics are easy to plug in.

| # | Statistic | bpf | expat | pcre | tcpdump | udhcpd |
|---|---|---|---|---|---|---|
| 1 | Symbolic input size (bytes) | 96 | 10 | 16 | 84 | 548 |
| 2 | Total statements run (not unique) | 298,195 | 41,345 | 423,182 | 40,097 | 15,258 |
| 3 | % of statements symbolic | 29.2% | 8.5% | 34.7% | 41.7% | 23.6% % |
| 4 | Explicit symbolic branch points | 77,024 | 1,969 | 98,138 | 11,425 | 888 |
| 5 | % with both branches feasible | 11.3% | 19.3% | 0.9% | 19.4% | 52.8% |
| 6 | Avg. # symbolic branches per path | 38.33 | 43.44 | 55.72 | 103.37 | 200.14 |
| 7 | Symbolic checks | 1,490 | 904 | 4,451 | 552 | 1,535 |
| 8 | Pointer concretizations | 0 | 0 | 0 | 73 | 0 |
| 9 | Symbolic args to uninstr. calls | 0 | 0 | 0 | 0 | 0 |

Table 5: Dynamic counts from EXE execution runs.

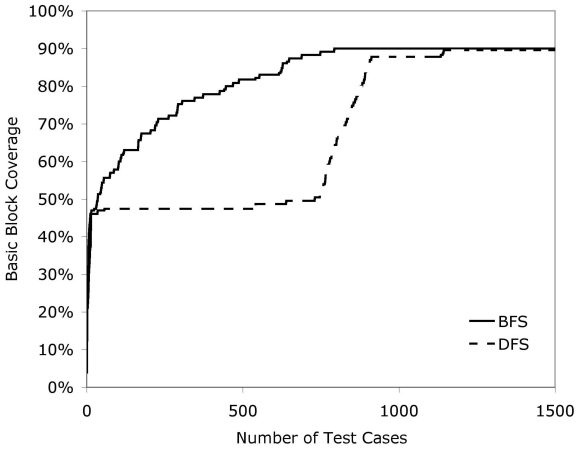


Figure 4: Best-first search vs. depth-first search.

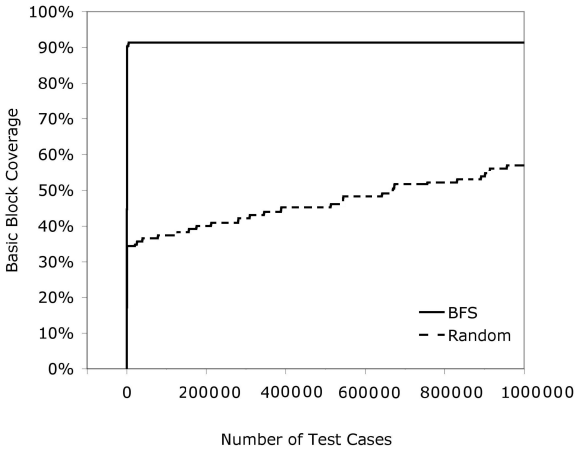


Figure 5: EXE with best-first search vs. random testing.

We experimentally evaluate our best-first search (BFS) heuristic in the context of one of our benchmarks, the Berkeley Packet Filter (BPF) (described in more detail in § 5.1). We start two separate executions of EXE, one using DFS and the other using BFS. We let both EXE executions run until they achieved full basic block coverage. Figure 4 compares BFS to DFS in terms of basic block coverage. (For visual clarity the graph only shows block coverage for the first 1500 test cases, as only a few blocks are missing from the coverage by these test cases.) BFS converges to full coverage more than twice as fast as DFS: 7,956 test cases versus 18,667. More precisely, EXE gets 91.74% block coverage, since there are several basic blocks in BPF that EXE cannot reach, such as dead code (e.g. the failure branch of asserts), or branches that do not depend on the input marked as symbolic.

Figure 5 then compares EXE against random testing also in terms of basic block coverage. We generate one million random test cases of the same size as those generated by EXE, and run these random test cases through a lightly-instrumented version of BPF that records basic block coverage. These test cases only cover 56.96% of the blocks in BPF; EXE achieves the same coverage in only 75 tests when using BFS. Even more strikingly, these million random test cases yield only 131 unique paths through the code, while each of EXE's test cases represents a unique path.

# 5. USING EXE TO FIND BUGS

This section presents three case studies that use EXE to find bugs in: (1) two packet filter implementations, (2) the `udhcpd` DHCP server, and (3) the `pcre` Perl compatible regular expressions library. We also summarize a previous effort of applying EXE to file system code.

## 5.1 Packet filters

Many operating systems allow programs to specify packet filters which describe the network packets they want to receive. Most packet filter implementations are variants of the Berkeley Packet Filter (BPF) system. BPF filters are written in a pseudo-assembly language, downloaded into the kernel, validated by the BPF system, and then applied to incoming packets. We used EXE to check the packet filter in both FreeBSD and Linux. FreeBSD uses BPF, while Linux uses a heavily modified version of it. EXE found two buffer overflows in the former and four errors in the latter. BPF is a particularly hard test of EXE — small, heavily-inspected and mature code, written by programmers known for their skill.

A filter is an array of instructions specifying an opcode (`code`), a possible memory offset to read or write (`k`), and several other fields. The BPF interpreter iterates over this filter, executing each opcode's corresponding action. This loop is the main source of vulnerabilities but is hard to test exhaustively (e.g., hitting all opcodes even once using random testing takes a long time).

We used a two-part checking process. First, we marked a fixed-sized array of filter instructions as symbolic and passed it to the packet filter validation routine `bpf_validate`, which returns `1` if it considers a filter legal. For each valid filter, we then mark a fixed-size byte array (representing a packet) as symbolic and run the filter interpreter `bpf_filter` on the symbolic filter with the symbolic packet, thus checking the

```
s[0].code = BPF_STX; // also: (BPF_LDX|BPF_MEM)
s[0].k    = 0xfffffff0UL;
s[1].code = BPF_RET;
```

Figure 6: A BPF filter of death

```
// Code extracted from bpf_validate. Rejects filter
// if memory opcode's reference beyond BPF_MEMWORDS.
// Forgets to check LDX,STX!
if((BPF_CLASS(p->code) == BPF_ST
  || (BPF_CLASS(p->code) == BPF_LD &&
       (p->code & 0xe0) == BPF_MEM))
  && p->k >= BPF_MEMWORDS )
  return 0;
...
// Code extracted from bpf_filter: pc points to current
// instruction. Both cases can overflow mem[pc->k].
   case BPF_LDX|BPF_MEM:
      X = mem[pc->k]; continue;
   ...
   case BPF_STX:
      mem[pc->k] = X; continue;
```

Figure 7: The BPF code Figure 6's filter exploits.

filter against all possible data packets of that length.

This checking illustrates one of EXE's interesting features: it turns interpreters into generators of the programs they can interpret. In our example, running the BPF interpreter on a symbolic filter causes it to generate all possible filters of that length, since each branch of the interpreter will fork execution, adding a constraint corresponding to the opcode it checked.

Figure 6 shows one of the two filters EXE found that cause buffer overflows in FreeBSD's BPF. The bug can occur when the opcode of a BPF instruction is either `BPF_STX` or `BPF_LDX | BPF_MEM`. As shown in Figure 7, `bpf_validate` forgets to bounds check the memory offset given by these instructions, as it does for instructions with opcodes `BPF_ST` or `BPF_LD | BPF_MEM`. This missing check means these instructions can write or read arbitrary offsets of the fixed-sized buffer `mem`, thus crashing the kernel or allowing a trivial exploit.

Linux had a trickier example. EXE found three filters that can crash the kernel because of an arithmetic overflow in a bounds check, shown in Figure 8. As with BPF, the offset field (`k`) caused the problem. Here, the code to interpret `BPF_LD` instructions eventually calls the function `skb_header_pointer`, which computes an offset into a given packet's data and returns it. This routine is passed `s[0].k` as the `offset` parameter, and values `4` or `2` as the `len` parameter. It extracts the size of the current message header

```
// other filters that cause this error:
//      code = (BPF_LD|BPF_B|BPF_IND)
//      code = (BPF_LD|BPF_H|BPF_IND)
s[0].code = BPF_LD|BPF_B|BPF_ABS;
s[0].k    = 0x7fffffffUL;
s[1].code = BPF_RET;
s[1].k    = 0xfffffff0UL;
```

Figure 8: A Linux filter of death

```
static inline void *
skb_header_pointer(struct sk_buff *skb,
          int offset, int len, void *buffer) {

  int hlen = skb_headlen(skb);

  // Memory overflow. offset=s[0].k; a filter
  // can make this value very large, causing
  // offset + len to overflow, trivially passing
  // the bounds check.
  if (offset + len <= hlen)
      return skb->data + offset;
```

Figure 9: The Linux code Figure 8's filter exploits.

into `hlen` and checks that `offset + len` $\leq$ `hlen`. However, the filter can cause `offset` to be very large, which means the signed addition `offset + len` will overflow to a small value, passing the check, but then causing that very large `offset` value to be added to the message `data` pointer. This allows attackers to easily crash the machine. This error would be hard to find with random testing. Its occurance in highly-visible, widely-used code, demonstrates that such tricky cases can empirically withstand repeated manual inspection.

## 5.2 A complete server: udhcpd

We also checked `udhcpd-0.9.8`, a clean, well-tested user-level DHCP server. We marked its input packet as symbolic, and then modified its network read call to return a packet of at most 548 bytes. After running `udhcpd` long enough to generate 596 test cases, EXE detected five different memory errors: four-byte read overflows at lines 213 and 214 in `dhcpd.c` and three similar errors at lines 79, 94, and 99 in `options.c`. These errors were not found when we tested the code using random testing. EXE generated packets to trigger all of these errors, one of which is shown in Figure 10. We confirmed these errors by rerunning the concrete error packets on an uninstrumented version of `udhcpd` while monitoring it with `valgrind`, a tool that dynamically checks for some types of memory corruption and storage leaks.

## 5.3 Perl Compatible Regular Expressions

The `pcre` library [37] is used by several popular open-source projects, including Apache, PHP, and Postfix. For speed, `pcre` provides a routine `pcre_compile`, which compiles a pattern string into a regular expression for later use. This routine has been the target of security advisories in the past [38].

We checked this routine by marking a null-terminated pattern string as symbolic and then passing it to `pcre_compile`. EXE quickly found a class of issues with this routine in a recent version of PCRE (6.6). The function iterates over the provided pattern twice, first to do basic error checking and to estimate how much memory to allocate for the compiled pattern, and second to do actual compilation. The bugs found included overflowing reads in the `check_posix_syntax` helper function (pcre_compile.c:1361-1363), called during the first pass, as well as more dangerous overflowing reads and writes in the `compile_regex` and `compile_branch` helpers (illegal writes on pcre_compile.c lines 3400-3401 and 3515-3616), which are called during the compilation pass. While

| *Offset* | *Hex value* |
|---|---|
| 0000 | 0000 0000 0000 0000 0000 0000 0000 0000 |
| 0010 | 0000 0000 0000 0000 0000 0000 5A00 0000 |
| .... | .... |
| 00F0 | 2100 00F9 0000 0000 0000 0000 0000 0000 |
| .... | .... |
| 01E0 | 0000 0000 0000 0000 0000 0000 2734 0000 |
| 01F0 | 0000 0000 0000 0000 0000 0000 0000 0000 |
| 0200 | 0000 0000 0000 0000 0000 0000 0000 3500 |
| 0210 | 030F 0000 0000 0000 0000 0000 0000 0000 |
| 0220 | 0032 0036 |

Figure 10: An EXE generated packet that causes an out-of-bounds read in `udhcpd`.

```
[^[\0^\0]\*−?]{\0          [\−\'[\0^\0]\']{\0
[\*−\'[\0^\0]\'−?]{\0      [\*−\'[\0^\0]\'−?]\0
[\*−\'[\0^\0]\'−?]\0       [\−\'[\0^\0]\'−]\0
(?#)\?[[[\0\0]\−]{\0       (?#)\?[[[\0\0]\−]\0
(?#)\?[[[\0\0]\[]\0        (?#)\?[:[[\0\0]\−]\0
(?#)\?[[[\0\0]\−]\0        (?#)\?[[[\0\0]\]\0
(?#)\?[[[\0\0][\0^\0]]\0   (?#)\?[[[\0\0][\0^\0]−]\0
(?#)\?[[[\0\0][\0^\0]\]\0  (?#)\?[=[[\0\0][\0^\0]\?]\0
```

Figure 11: EXE-generated regular expression patterns that cause out-of-bounds writes (leading to aborts in `glibc` on free) when passed as the first argument to `pcre_compile`.

the first problem may appear to be an innocent read past the end of the buffer, it allows illegal expressions to enter the second pass, causing more serious issues. The substring "`[\0^\0]`" is especially dangerous because strings which end with this sequence will cause `pcre` to skip over both null characters and continue parsing unallocated or uninitialized memory. Figure 11 show a representative sample of EXE-generated patterns that trigger overflows in `pcre`, which in turn cause `glibc` aborts. The author of the library fixed the bug soon after being notified, and so the lastest version of `pcre` as of this writing (6.7) does not exhibit this problem.

## 5.4 Generating disks of death

We previously used EXE to generate disk images for three file systems (`ext2`, `ext3`, and `JFS`) that when mounted would crash or compromise the Linux kernel [44]. At a high level, the checking worked as follows. We wrote a special device driver that returned symbolic blocks to its callers. We then compiled Linux using EXE and ran it as a user-level process (so `fork` would work) and invoked the `mount` system call, which caused the file system to read symbolic blocks, thereby driving checking.

We found bugs in all three file systems, demonstrating that EXE can handle complex systems code. Further, these errors would almost certainly be beyond the reach of random testing. For example, the Linux `ext2` "read super block" routine has over forty if-statements to check the data associated with the super block. Any randomly-generated super block must satisfy these tests before it can reach even the next level of error checking, much less triggering the execution of "real code" that performs actual file system operations.

# 6. RELATED WORK

We described an initial, primitive version of EXE (then called EGT) in an invited workshop paper [12]. EGT did not support reads or writes of symbolic pointer expressions, symbolic arrays, bit-fields, casting, sign-extension, arithmetic overflow, and our symbolic checks. We also gave an overview of EXE in the file system checking paper [44] discussed in Section 5.4. That paper took EXE as a given and used it to find bugs. In contrast, both STP and EXE are contributions of this paper, which we describe in more detail as well as focusing on a broader set of applications.

Simultaneously with our initial work [12], DART [26] also generated test cases from symbolic inputs. DART runs the tested unit code on random input and symbolically gathers constraints at decision points that use input values. Then, DART negates one of these symbolic constraints to generate the next test case. DART only handles integer constraints and devolves to random testing when pointer constraints are used, with the usual problems of missed paths.

The CUTE project [40] extends DART by tracking symbolic pointer constraints of the form: `p = NULL`, `p` $\neq$ `NULL`, `p = q`, or `p` $\neq$ `q`. In addition, CUTE tracks constraints formed by reading or writing symbolic memory at constant offsets (such as a field dereference `p`→`field`), but unlike EXE cannot handle symbolic offsets. For example, the paper on CUTE shows that on the code snippet `a[i] = 0; a[j] = 1; if (a[i] == 0) ERROR`, CUTE fails to find the case `i == j`, which would have driven the code down both paths. In contrast to both DART and CUTE, EXE has completely accurate constraints on memory, and thus can (potentially) check code much more thoroughly.

CBMC is a bounded model checker for ANSI-C programs [13] designed to cross-check an ANSI C re-implementation of a circuit against its Verilog implementation. Unlike EXE, which uses a mixture of concrete and symbolic execution, CBMC runs code entirely symbolically. It takes (and requires) an entire, strictly-conforming ANSI C program, which it translates into constraints that are passed to a SAT solver. CBMC provides full support for C arithmetic and control operations, as well as reads and writes of symbolic memory. However, it has several serious limitations. First, it has a strongly-typed view of memory, which prevents it from checking code that accesses memory through pointers of different types. Second, because CBMC must translate the entire program to SAT, it can only check stand-alone programs that do not interact with the environment (e.g., by using system calls or even calling code for which there is no source). Both of these limits seem to prevent CBMC from checking the applications in this paper. Finally, CBMC unrolls all loops and recursive calls, which means that it may miss bugs that EXE can find and also that it may execute some symbolic loops more times than the current set of constraints allows.

Larson and Todd [33] present a system that dynamically tracks primitive constraints associated with "tainted" data (e.g., data that comes from untrusted sources such as network packets) and warns when the data could be used in a potentially dangerous way. They associate tainted integers with an upper and lower bound and tainted strings with their maximum length and whether the string is null-terminated. At potentially dangerous uses of inputs, such as array references or calls to the string library, they check whether the integer could be out of bounds, or if the string

could violate the library function's contract. Thus, as EXE, this system can detect an error even if it did not actually occur during the program's concrete execution. However, their system lacks almost all of the symbolic power that EXE provides. Further, they cannot generate inputs to cause paths to be executed; users must provide test cases and they can only check paths covered by these test cases.

**Static checking.** There has been much recent work on static bug finding, including both better type systems [19, 24, 22] and static analysis tools [24, 5, 17, 18, 23, 11, 41]. The insides of these tools look dramatically different from EXE. An exception is Saturn [42], which expresses program properties as boolean constraints and models pointers and heap data down to the bit level. Dynamic analysis requires running code, static analysis does not. Thus, static tools often take less work to apply (just compile the source and skip what cannot be handled), can check all paths (rather than only executed ones), and can find bugs in code it cannot run (such as operating systems code). However, because EXE runs code, it can check both much deeper properties, such as complex expressions in assertions, or properties that depend on accurate value information (the exact value of an index or size of an object), pointers, and heap layout, among many others. Further, unlike static analysis, EXE has no false positives. However, we view the two approaches as complementary: there is no reason not to use lightweight static techniques and then use EXE.

**Software Model Checking.** Model checkers have been used to find bugs in both the design and the implementation of software [30, 31, 8, 15, 5, 25, 45]. These approaches often require a lot of manual effort to build test harnesses. However, to some degree, the approaches are complementary to EXE: the tests EXE generates could be used to drive the model checked code, similar to the approach embraced by the Java PathFinder (JPF) project [32]. JPF combines model checking and symbolic execution to check applications that manipulate complex data structures written in Java. JPF differs from EXE in that it does not have support for untyped memory (not needed because Java is a strongly typed language) and does not support symbolic pointers.

**Static input generation.** A long stream of research tries to statically solve constraints to generate inputs that would cause execution to reach a specific program point or path [7, 27, 2, 4, 9]. In both theory and practice, static input generation is much weaker than dynamic techniques such as EXE, which has ready access to useful information impossible to get without running the program.

**Dynamic techniques for test and input generation.** Past dynamic input generation work seem to focus on generating an input to follow a specific path, motivated by the problem of answering programmer queries as to whether control can reach a specific statement or not [21, 28]. EXE instead focuses on bug finding, in particular the problems of exhausting all input-controlled paths and universal checking, neither addressed by prior work.

# 7. CONCLUSION

We have presented EXE, which uses robust, bit-level accurate symbolic execution to find deep errors in code and automatically generate inputs that will hit these errors. A key aspect of EXE is its modeling of memory and its co-designed, fast constraint solver STP. We have applied EXE to a variety of real, tested programs where it was powerful enough to uncover subtle and surprising bugs.

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# 8. REFERENCES

[1] SMTLIB competition. http://www.csl.sri.com/users/demoura/smt-comp/, August 2006.

[2] T. Ball. A theory of predicate-complete test coverage and generation. In *Proceedings of the Third International Symposium on Formal Methods for Components and Objects*, Nov. 2004.

[3] T. Ball and R. B. Jones, editors. *Computer Aided Verification, 18th International Conference, CAV 2006, Seattle, WA, USA, August 17-20, 2006, Proceedings*, volume 4144 of *Lecture Notes in Computer Science*. Springer, 2006.

[4] T. Ball, R. Majumdar, T. Millstein, and S. K. Rajamani. Automatic predicate abstraction of C programs. In *PLDI '01: Proceedings of the ACM SIGPLAN 2001 conference on Programming language design and implementation*, pages 203–213. ACM Press, 2001.

[5] T. Ball and S. Rajamani. Automatically validating temporal safety properties of interfaces. In *SPIN 2001 Workshop on Model Checking of Software*, May 2001.

[6] C. Barrett and S. Berezin. CVC Lite: A new implementation of the cooperating validity checker. In R. Alur and D. A. Peled, editors, *CAV*, Lecture Notes in Computer Science. Springer, 2004.

[7] R. S. Boyer, B. Elspas, and K. N. Levitt. Select – a formal system for testing and debugging programs by symbolic execution. *ACM SIGPLAN Notices*, 10(6):234–45, June 1975.

[8] G. Brat, K. Havelund, S. Park, and W. Visser. Model checking programs. In *IEEE International Conference on Automated Software Engineering (ASE)*, 2000.

[9] D. Brumley, J. Newsome, D. Song, H. Wang, and S. Jha. Towards automatic generation of vulnerability-based signatures. In *Proceedings of the 2006 IEEE Symposium on Security and Privacy*, 2006.

[10] R. E. Bryant, S. K. Lahiri, and S. A. Seshia. Modeling and verifying systems using a logic of counter arithmetic with lambda expressions and uninterpreted functions. In E. Brinksma and K. G. Larsen, editors, *Proc. Computer-Aided Verification (CAV)*, pages 78–92. Springer-Verlaag, July 2002.

[11] W. Bush, J. Pincus, and D. Sielaff. A static analyzer for finding dynamic programming errors. *Software: Practice and Experience*, 30(7):775–802, 2000.

[12] C. Cadar and D. Engler. Execution generated test cases: How to make systems code crash itself. In *Proceedings of the 12th International SPIN Workshop on Model Checking of Software*, August 2005. A longer version of this paper appeared as Technical Report CSTR-2005-04, Computer Systems Laboratory, Stanford University.

[13] E. Clarke and D. Kroening. Hardware verification using ANSI-C programs as a reference. In *Proceedings of ASP-DAC 2003*, pages 308–311. IEEE Computer Society Press, January 2003.

[14] B. Cook, D. Kroening, and N. Sharygina. Cogent: Accurate theorem proving for program verification. In K. Etessami and S. K. Rajamani, editors, *Proceedings of CAV 2005*, volume 3576 of *Lecture Notes in Computer Science*, pages 296–300. Springer Verlag, 2005.

[15] J. Corbett, M. Dwyer, J. Hatcliff, S. Laubach, C. Pasareanu, Robby, and H. Zheng. Bandera: Extracting finite-state models from Java source code. In *ICSE 2000*, 2000.

[16] T. H. Cormen, C. E. Leiserson, R. L. Rivest, and C. Stein. *Introduction to Algorithms*. The MIT Electrical Engineering

and Computer Science Series. MIT Press/McGraw Hill, 2001.

[17] SWAT: the Coverity software analysis toolset. http://coverity.com.

[18] M. Das, S. Lerner, and M. Seigle. Path-sensitive program verification in polynomial time. In *Proceedings of the ACM SIGPLAN 2002 Conference on Programming Language Design and Implementation*, Berlin, Germany, June 2002.

[19] R. DeLine and M. Fähndrich. Enforcing high-level protocols in low-level software. In *Proceedings of the ACM SIGPLAN 2001 Conference on Programming Language Design and Implementation*, June 2001.

[20] N. Een and N. Sorensson. An extensible sat-solver. In *Proc. Sixth International Conference on Theory and Applications of Satisfiability Testing*, pages 78–92, May 2003.

[21] R. Ferguson and B. Korel. The chaining approach for software test data generation. *ACM Trans. Softw. Eng. Methodol.*, 5(1):63–86, 1996.

[22] C. Flanagan and S. N. Freund. Type-based race detection for Java. In *SIGPLAN Conference on Programming Language Design and Implementation*, pages 219–232, 2000.

[23] C. Flanagan, K. Leino, M. Lillibridge, G. Nelson, J. Saxe, and R. Stata. Extended static checking for Java. In *Proceedings of the ACM SIGPLAN 2002 Conference on Programming Language Design and Implementation*, pages 234–245. ACM Press, 2002.

[24] J. Foster, T. Terauchi, and A. Aiken. Flow-sensitive type qualifiers. In *Proceedings of the ACM SIGPLAN 2002 Conference on Programming Language Design and Implementation*, June 2002.

[25] P. Godefroid. Model Checking for Programming Languages using VeriSoft. In *Proceedings of the 24th ACM Symposium on Principles of Programming Languages*, 1997.

[26] P. Godefroid, N. Klarlund, and K. Sen. DART: Directed automated random testing. In *Proceedings of the Conference on Programming Language Design and Implementation (PLDI)*, Chicago, IL USA, June 2005. ACM Press.

[27] A. Gotlieb, B. Botella, and M. Rueher. Automatic test data generation using constraint solving techniques. In *ISSTA '98: Proceedings of the 1998 ACM SIGSOFT international symposium on Software testing and analysis*, pages 53–62. ACM Press, 1998.

[28] N. Gupta, A. P. Mathur, and M. L. Soffa. Automated test data generation using an iterative relaxation method. In *SIGSOFT '98/FSE-6: Proceedings of the 6th ACM SIGSOFT International Symposium on Foundations of Software Engineering*, pages 231–244. ACM Press, 1998.

[29] R. Hastings and B. Joyce. Purify: Fast detection of memory leaks and access errors. In *Proceedings of the Winter USENIX Conference*, Dec. 1992.

[30] G. J. Holzmann. The model checker SPIN. *Software Engineering*, 23(5):279–295, 1997.

[31] G. J. Holzmann. From code to models. In *Proc. 2nd Int. Conf. on Applications of Concurrency to System Design*, pages 3–10, Newcastle upon Tyne, U.K., 2001.

[32] S. Khurshid, C. S. Pasareanu, and W. Visser. Generalized symbolic execution for model checking and testing. In *Proceedings of the Ninth International Conference on Tools and Algorithms for the Construction and Analysis of Systems*, 2003.

[33] E. Larson and T. Austin. High coverage detection of input-related security faults. In *Proceedings of the 12th USENIX Security Symposium, August 2003*.

[34] B. P. Miller, L. Fredriksen, and B. So. An empirical study of the reliability of UNIX utilities. *Communications of the Association for Computing Machinery*, 33(12):32–44, 1990.

[35] G. C. Necula, S. McPeak, S. Rahul, and W. Weimer. CIL: Intermediate language and tools for analysis and transformation of C programs. In *International Conference on Compiler Construction*, March 2002.

[36] G. Nelson and D. Oppen. Simplification by cooperating decision procedures. *ACM Transactions on Programming Languages and Systems*, 1(2):245–57, 1979.

[37] PCRE - Perl Compatible Regular Expressions. http://www.pcre.org/.

[38] PCRE Regular Expression Heap Overflow. US-CERT Cyber Security Bulletin SB05-334. http://www.us-cert.gov/cas/bulletins/SB05-334.html#pcre.

[39] O. Ruwase and M. S. Lam. A practical dynamic buffer overflow detector. In *Proceedings of the 11th Annual Network and Distributed System Security Symposium*, pages 159–169, 2004.

[40] K. Sen, D. Marinov, and G. Agha. CUTE: A concolic unit testing engine for C. In *In 5th joint meeting of the European Software Engineering Conference and ACM SIGSOFT Symposium on the Foundations of Software Engineering (ESEC/FSE'05), ACM (To appear)*, Sept. 2005.

[41] D. Wagner, J. Foster, E. Brewer, and A. Aiken. A first step towards automated detection of buffer overrun vulnerabilities. In *The 2000 Network and Distributed Systems Security Conference. San Diego, CA*, Feb. 2000.

[42] Y. Xie and A. Aiken. Scalable error detection using boolean satisfiability. In *Proceedings of the 32nd Annual Symposium on Principles of Programming Languages (POPL 2005), January 2005*.

[43] Y. Xie and A. Aiken. Saturn: A SAT-based tool for bug detection. In K. Etessami and S. K. Rajamani, editors, *CAV*, volume 3576 of *Lecture Notes in Computer Science*, pages 139–143. Springer, 2005.

[44] J. Yang, C. Sar, P. Twohey, C. Cadar, and D. Engler. Automatically generating malicious disks using symbolic execution, May 2006.

[45] J. Yang, P. Twohey, D. Engler, and M. Musuvathi. Using model checking to find serious file system errors. In *Symposium on Operating Systems Design and Implementation*, December 2004.